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FORM	First Named Inventor	Adolph Mondry
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Title of the Invention – The Voltage Dosimeter –

System and method for

supplying variable voltage

to an electric circuit.

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032 Referring now to Figure 6/6 a flow chart is shown illustrating representative calculations of Tss according to the present invention. One of these calculations or an analogous calculation is performed for each series state of Figure 3/6-5/6, such as illustrated at Steps 408, 411, and 412.

033 Returning to Figure 6/6 at Step 480 a test is performed to determine if the system has reached a base state. If not, the series state delay is estimated as: Tss=tr/IR. If the result is true, the process continues with Step 484, where a test is performed to determine whether v2<dL. If true, then Step 486 determines whether the most recent base state is a minimum for the current range. If it is true, the series state delay is calculated by Step 488 as Tss=tr/(IR-1). Step 498 then returns control to the series state which initiated the calculation.

034 With continuing reference to Figure 6/6, if the test at Step 486 is false then the series state delay is calculated by Step 490 as Tss=tr(MAX R-MIN R)/(IR-1)(MAX R-BASE STATE) before control is released to the series state via Step 498. If the test performed at Step 484 is false, then Step 492 performs a test to determine if the most recent base state is the maximum for the current range. If the result of Step 492 is true, then Step 496 calculates the series state delay as Tss=tr/(IR-1). Control is then returned to the appropriate series state via Step 498. If the result of the test at Step 492 is false, then the series state delay is calculated by Step 494 as Tss=tr(MAX R-

There are no arguments. On page 15 (now page 3) paragraph 034 line 9 of the patent application for the Voltage Dosimeter – the full name and the inventor's name, address, phone number, and citizenship appears on the accompanying cover sheet – the word **false** replaces the word **true**.

The sentence now reads – If the result of the test at Step **492** is false then the series state delay is calculated by Step **494** as Tss=tr(MAX R-MINR)/(IR-1)(BASE STATE-MIN R).

On page 15 (now page 3) paragraph 034 line 9 the word **false** replaces the word **true**.

then the series state delay is calculated by Step 490 as Tss=tr(MAX R-MIN R)/(IR-1)(MAX R-BASE STATE) before control is released to the series state via Step 498. If the test performed at Step 484 is false, then Step 492 performs a test to determine if the most recent base state is the maximum for the current range. If the result of Step 492 is true, then Step 496 calculates the series state delay as Tss=tr/(IR-1). Control is then returned to the appropriate series state via Step 498. If the result of the test at Step 492 is false, not true, then the series state delay is calculated by Step 494 as Tss=tr(MAX R-MIN R)/(IR-1)(BASE STATE-MIN R).